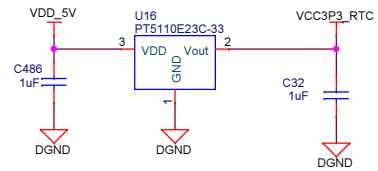
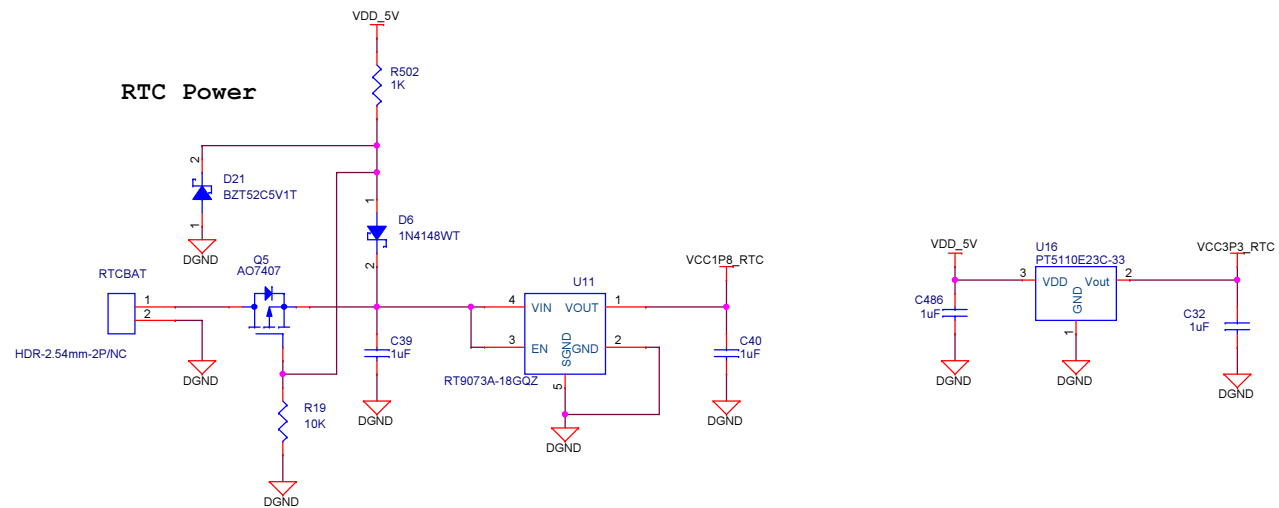
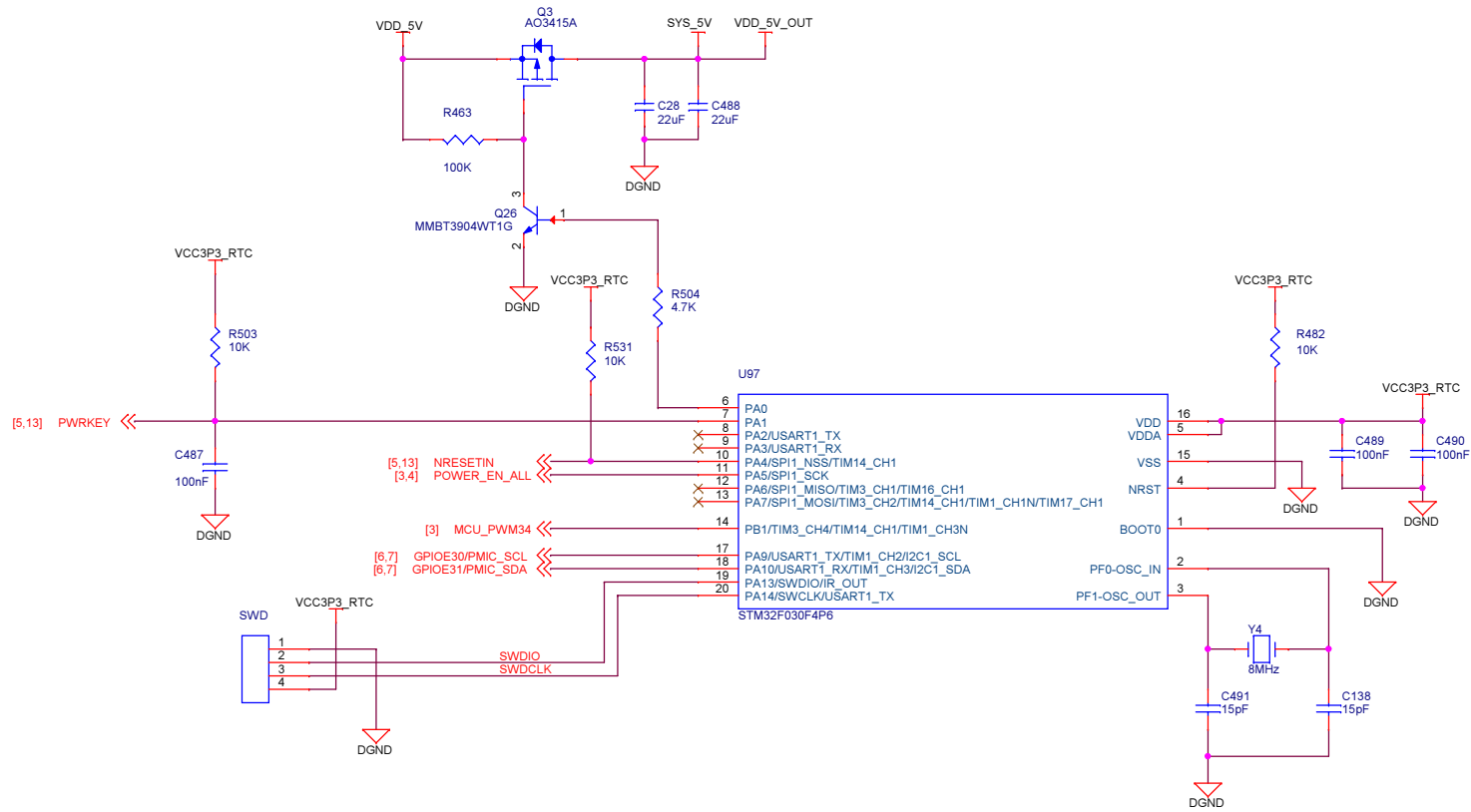


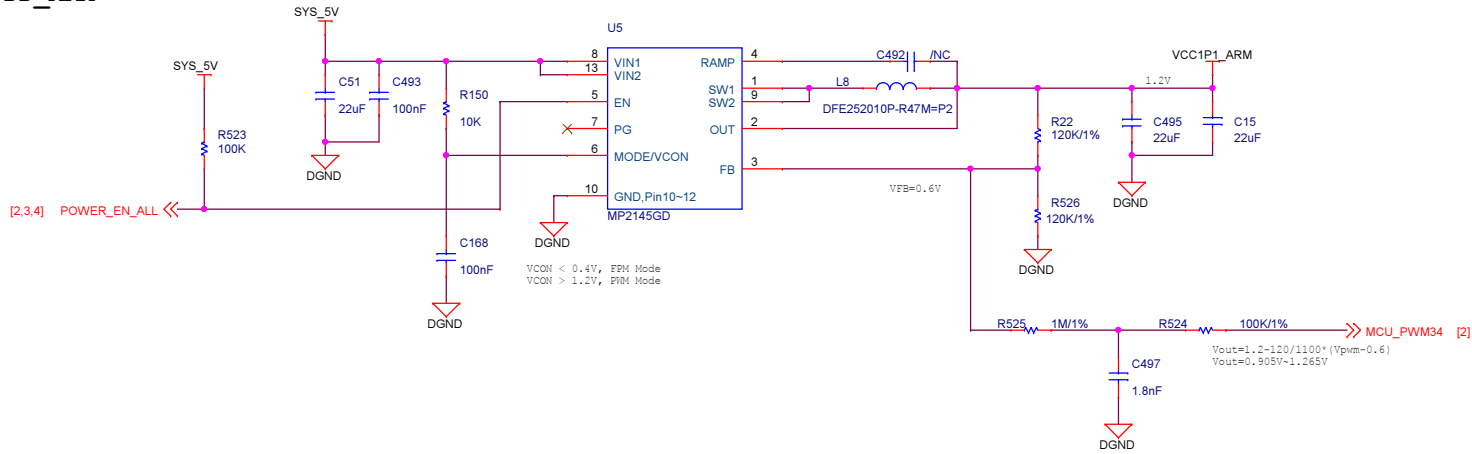
NanoPi S2



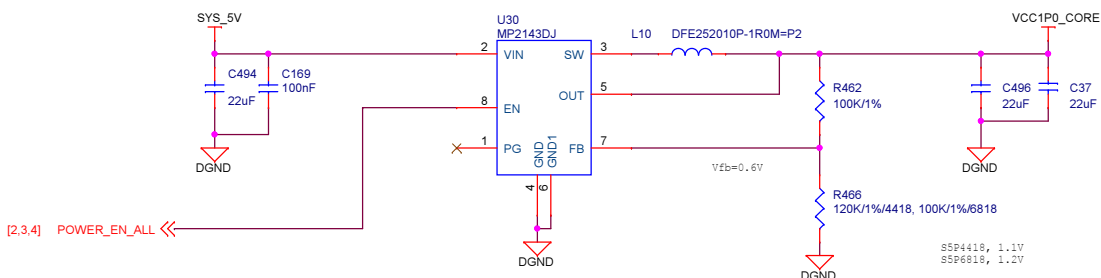
NanoPi S2		
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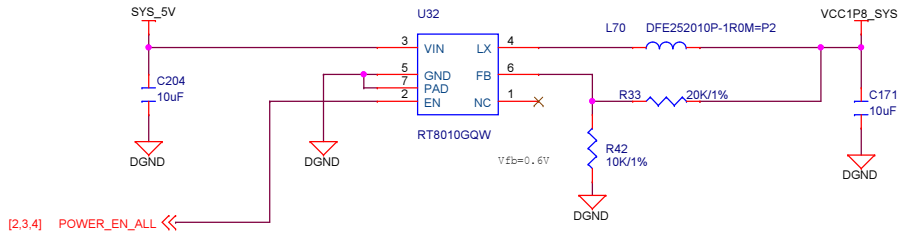
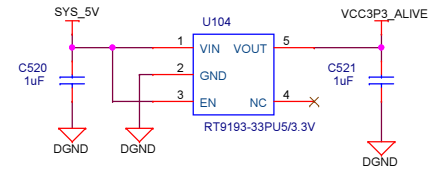
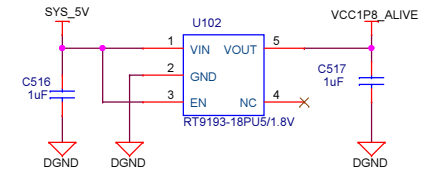
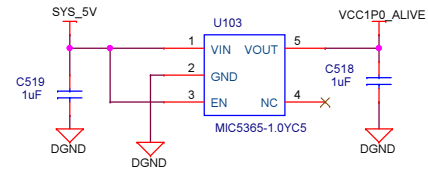
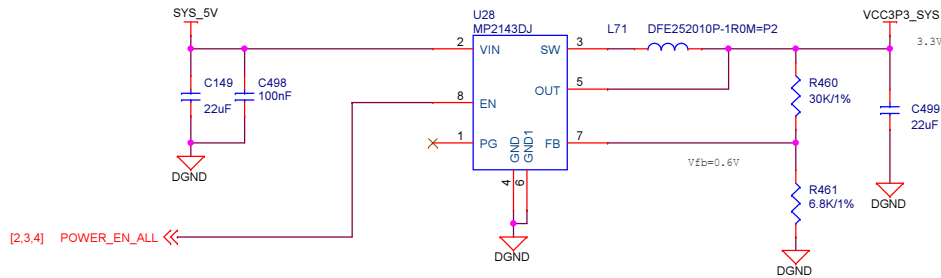
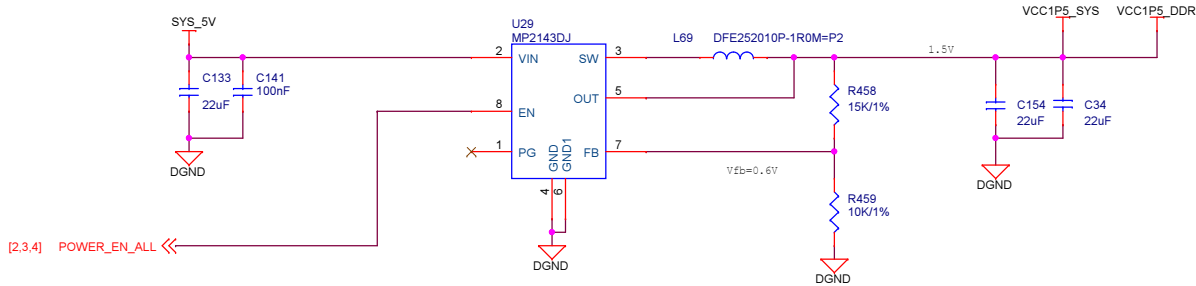
VDD_ARM



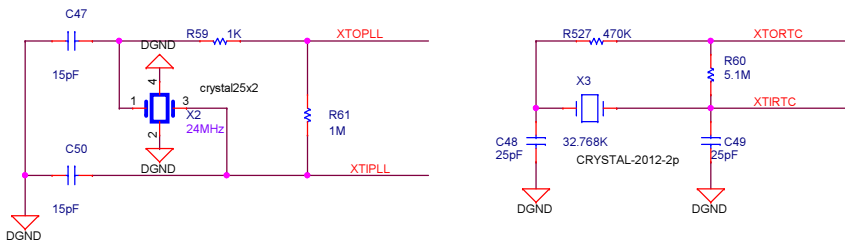
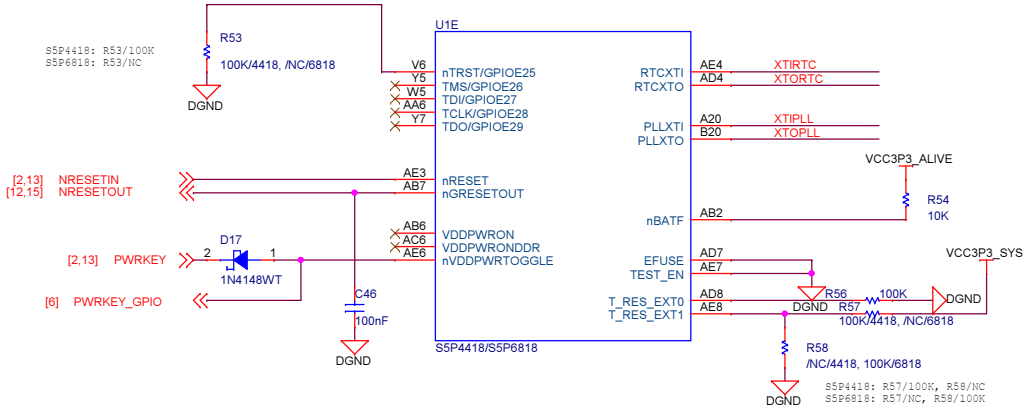
VDD_CORE



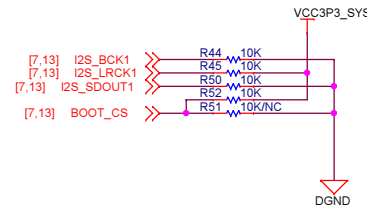
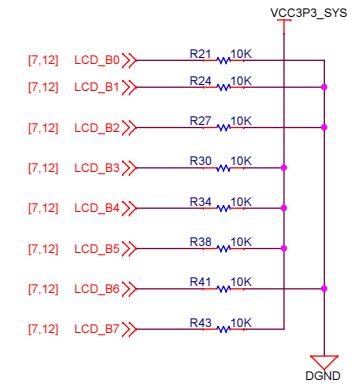
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System Reset, Clocks



Boot Mode Config



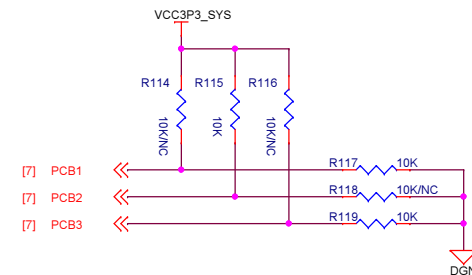
BOOT MODE OPTION

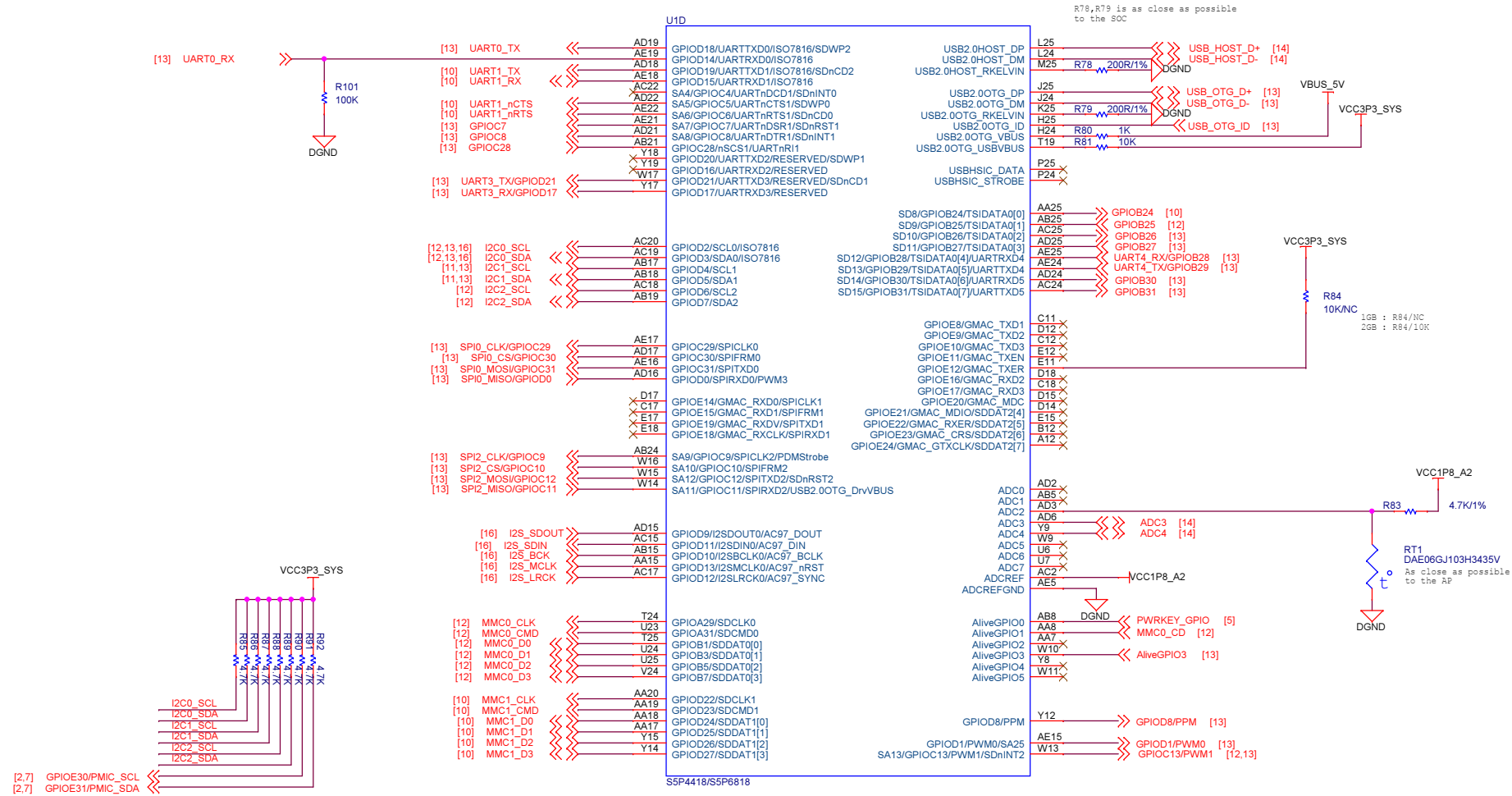
	eMMC	SPI	USB	NAND
SD0	HIGH	LOW	LOW	HIGH
SD1	LOW	LOW	HIGH	HIGH
SD2	HIGH	HIGH	HIGH	HIGH
SD4	LOW	HIGH		
SD5	LOW	LOW		

Boot media port select (SPI, eMMC)

	CH0	CH1	CH2
SD3	LOW	HIGH	LOW
CAM1_D3	LOW	LOW	HIGH

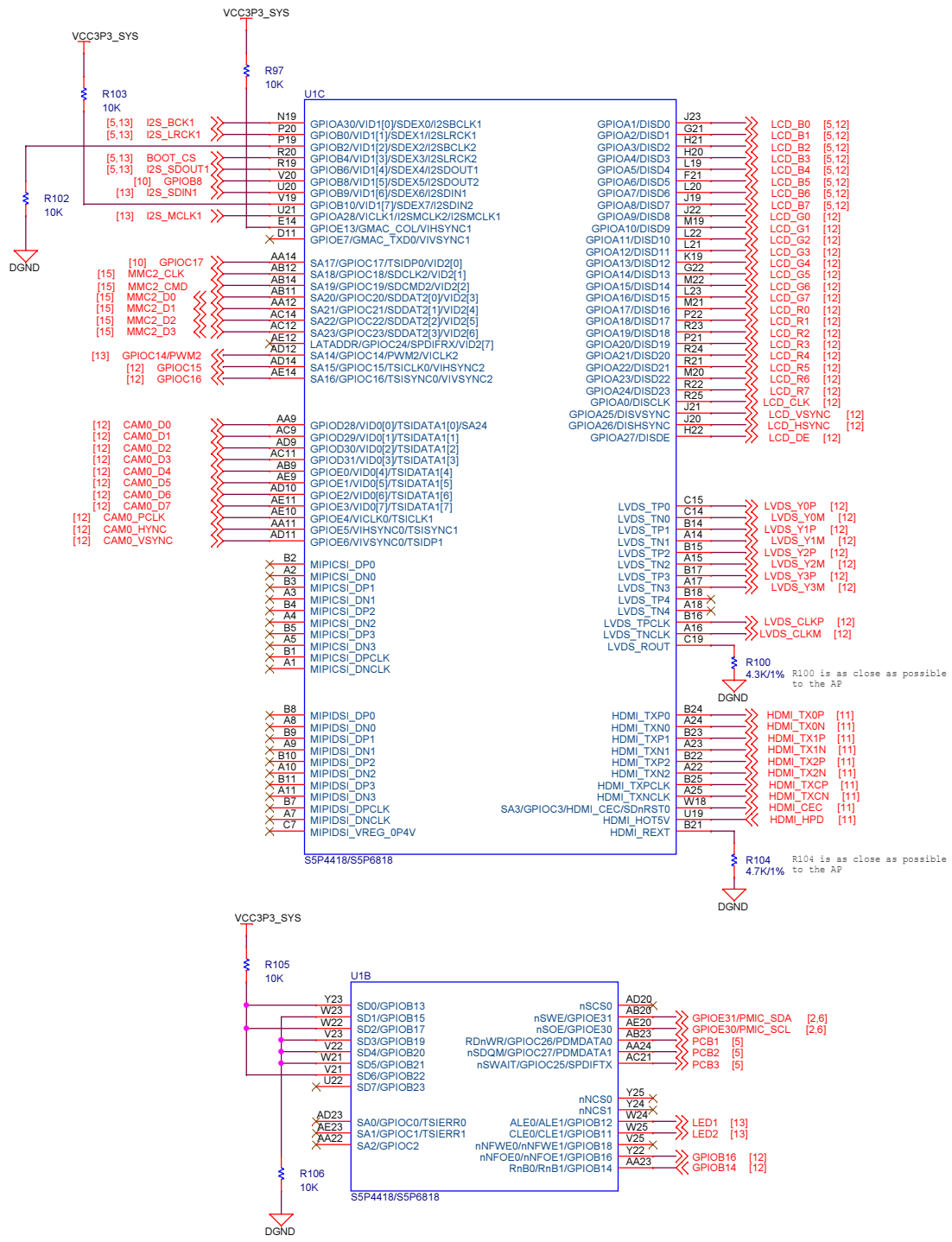
PCB Version



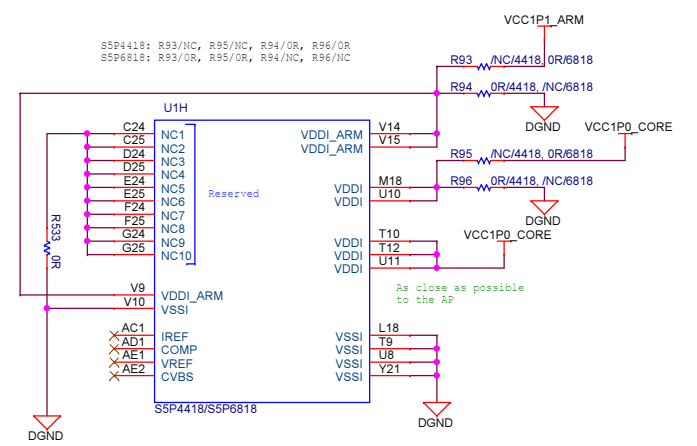


I2C CH0 : Camera
 I2C CH1 : HDMI EDID
 I2C CH2 : Touch
 PMIC_I2C : PMIC

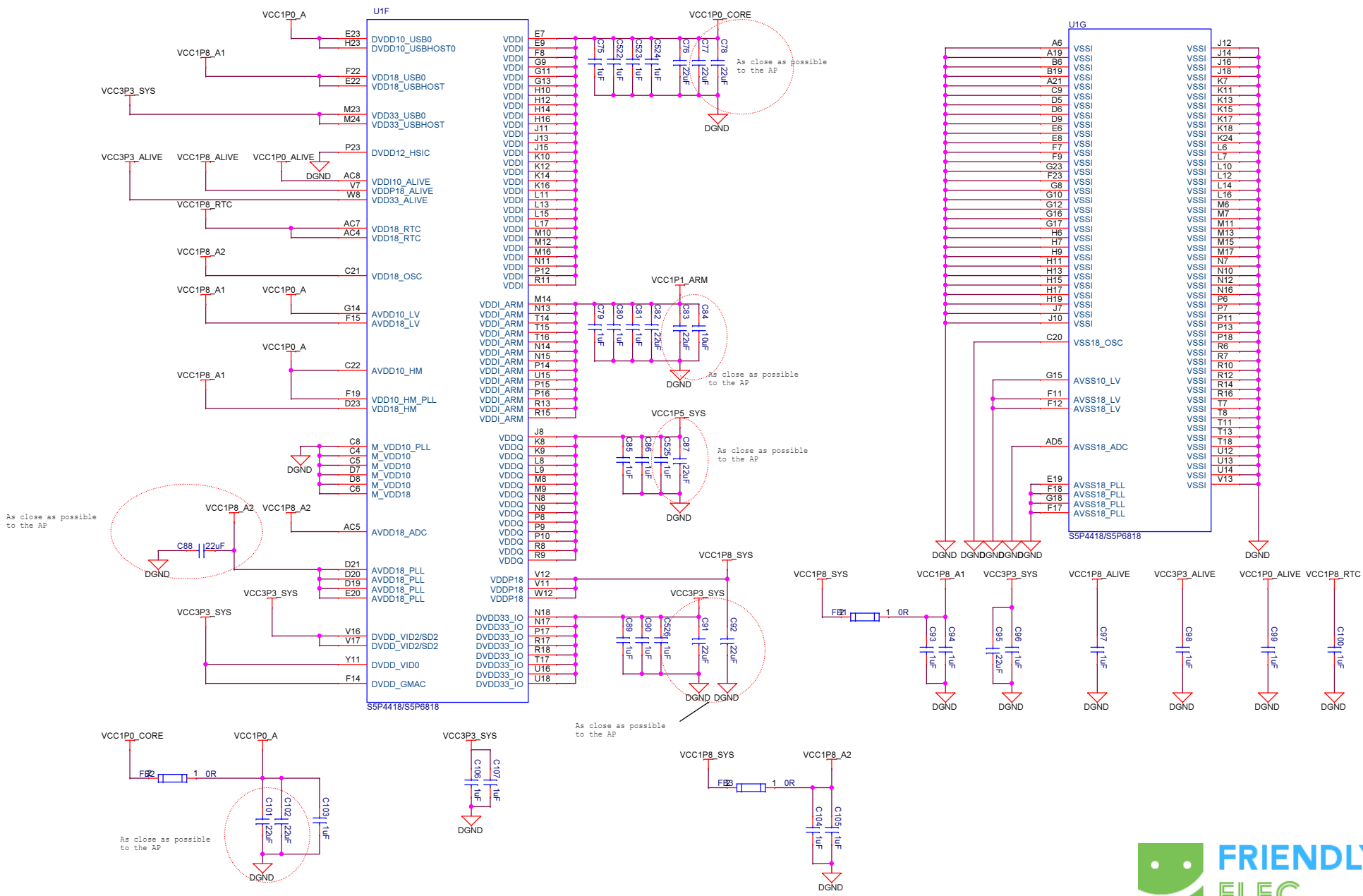




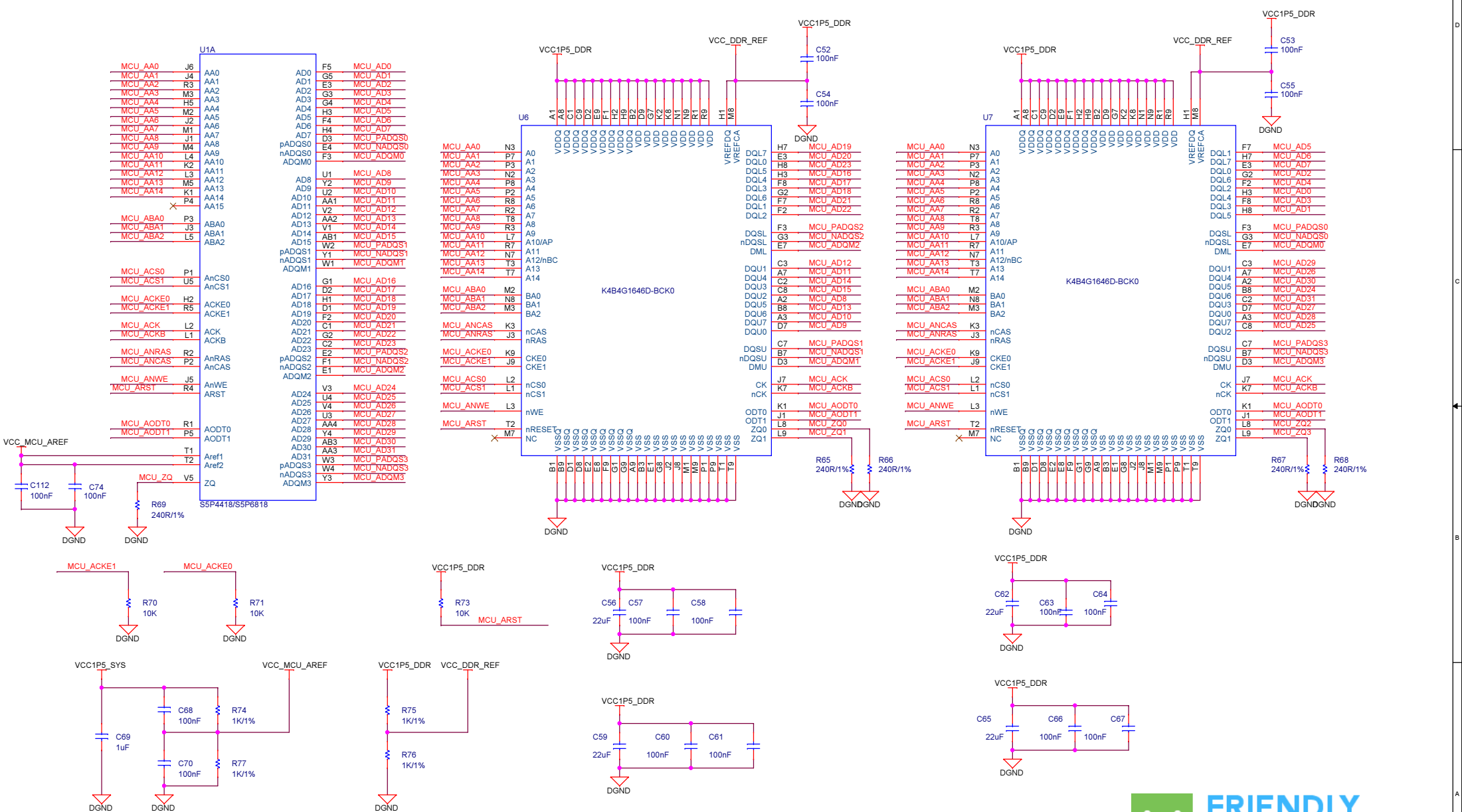
Reserved Function

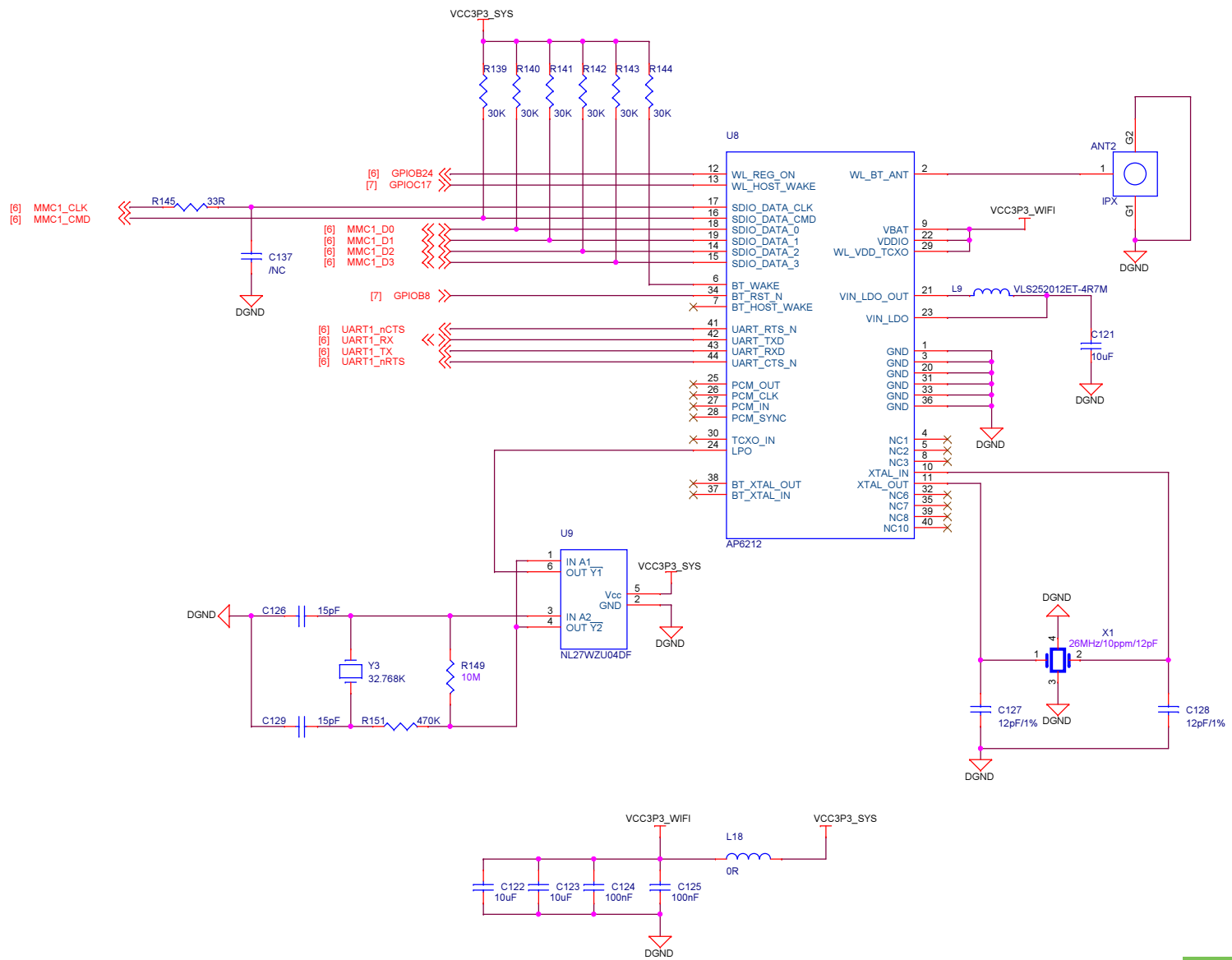


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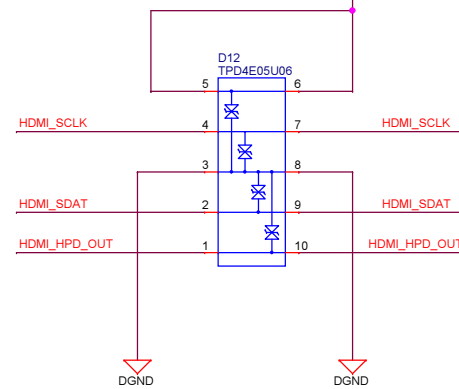
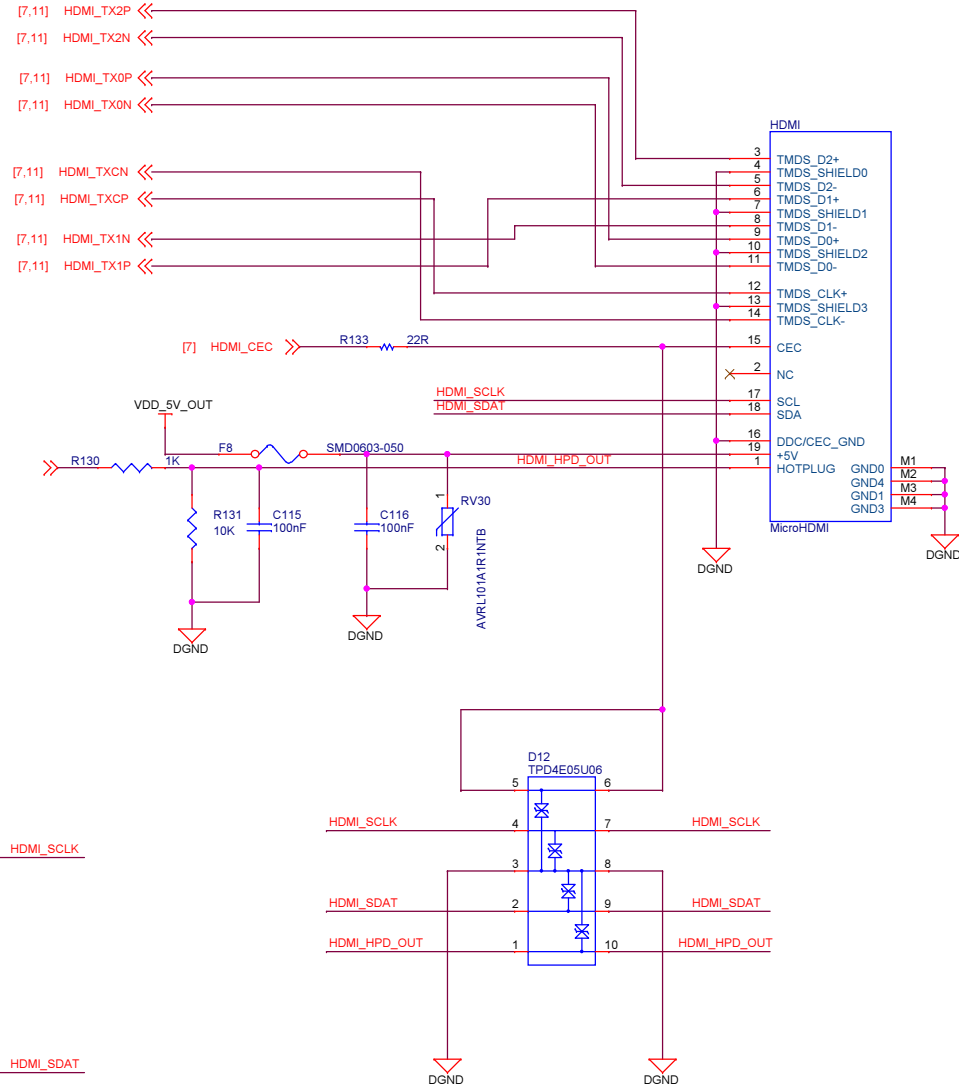
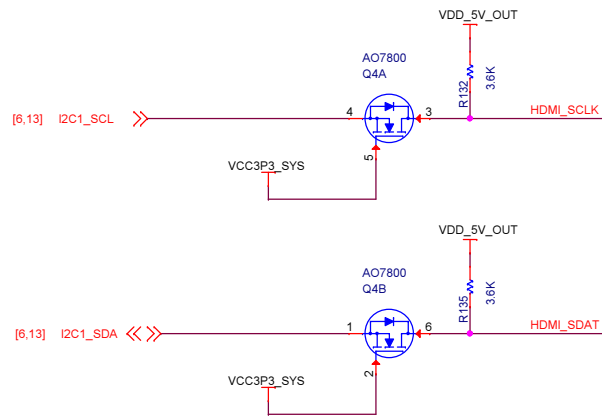
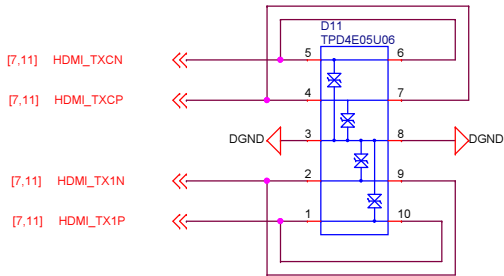
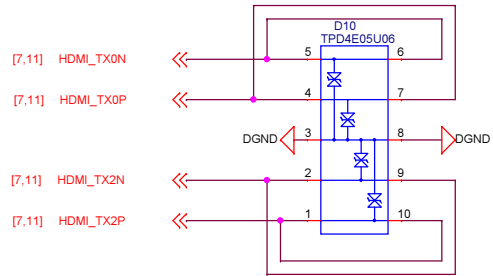


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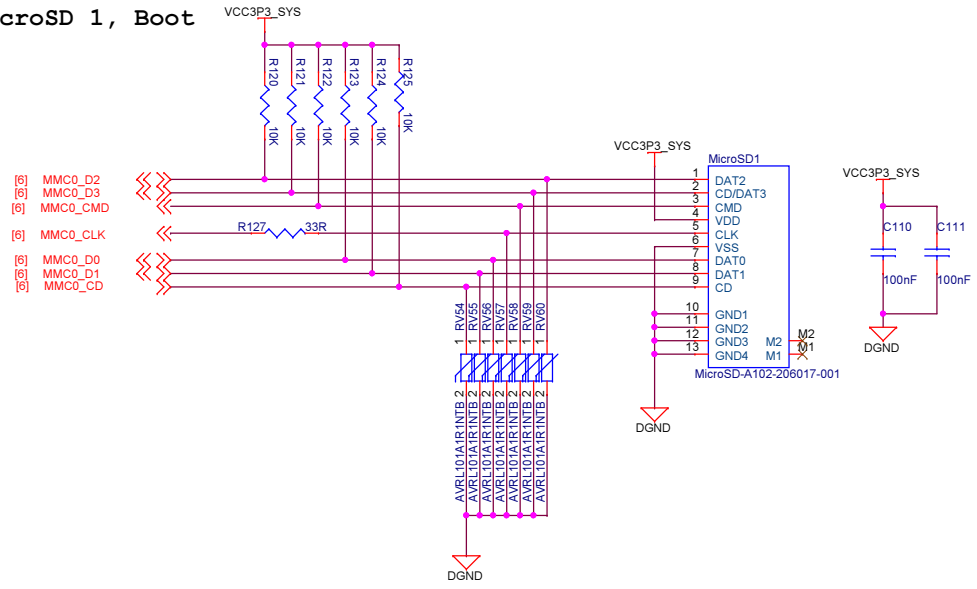




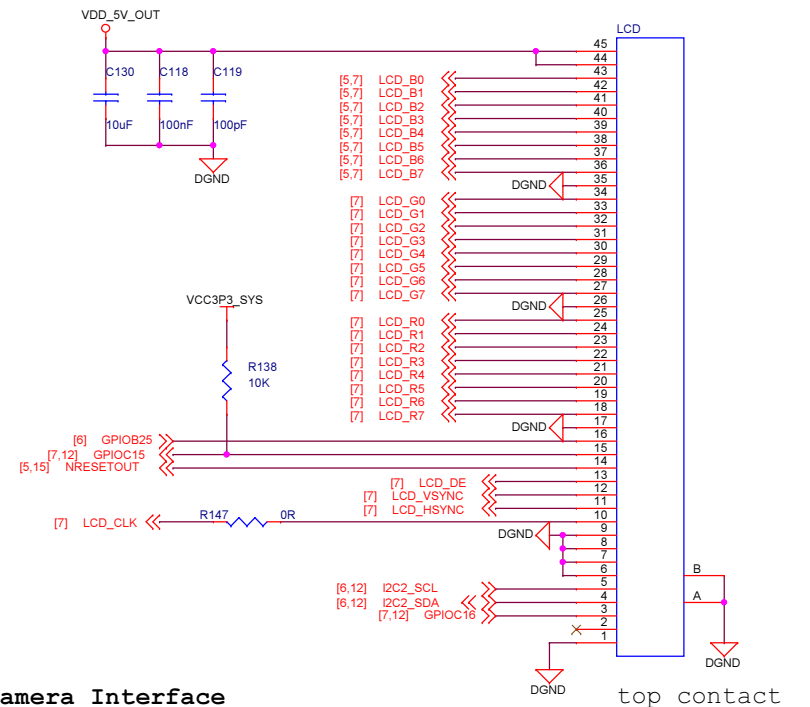
NanoPi S2		
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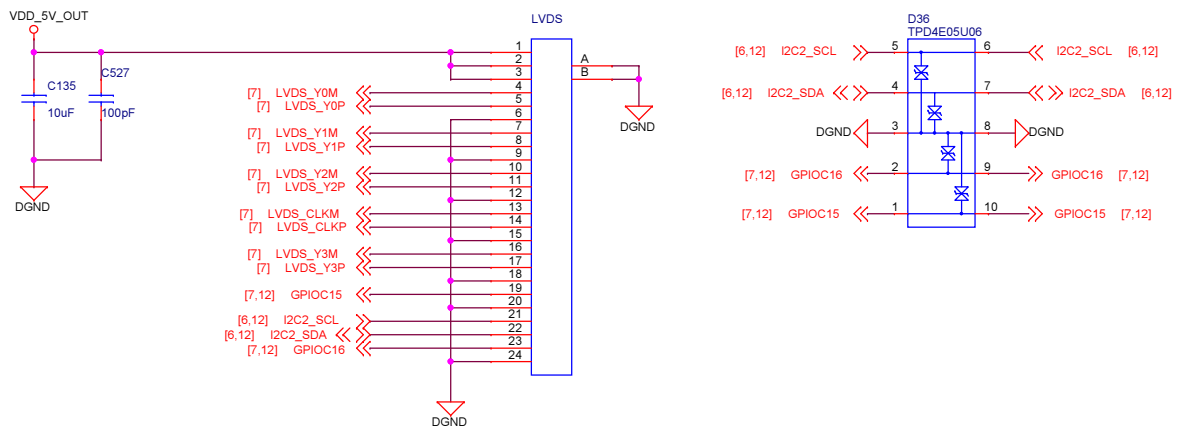
MicroSD 1, Boot



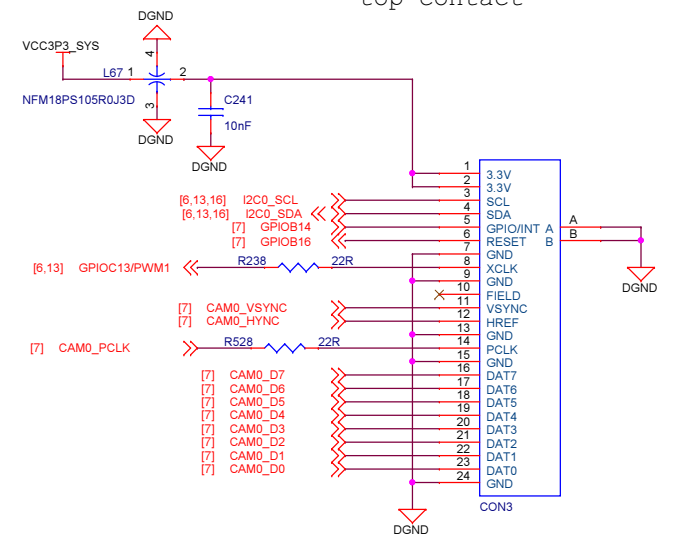
RGB LCD Interface



LVDS

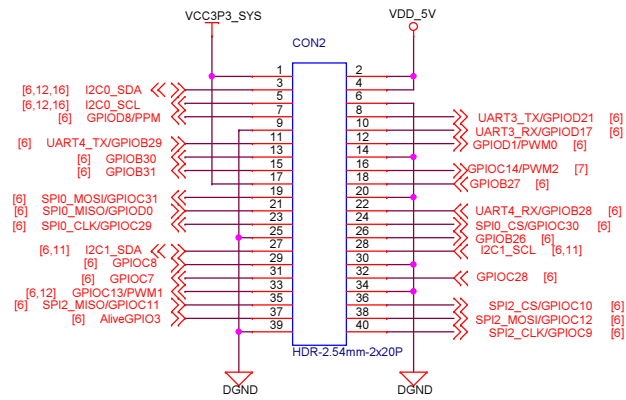


Camera Interface

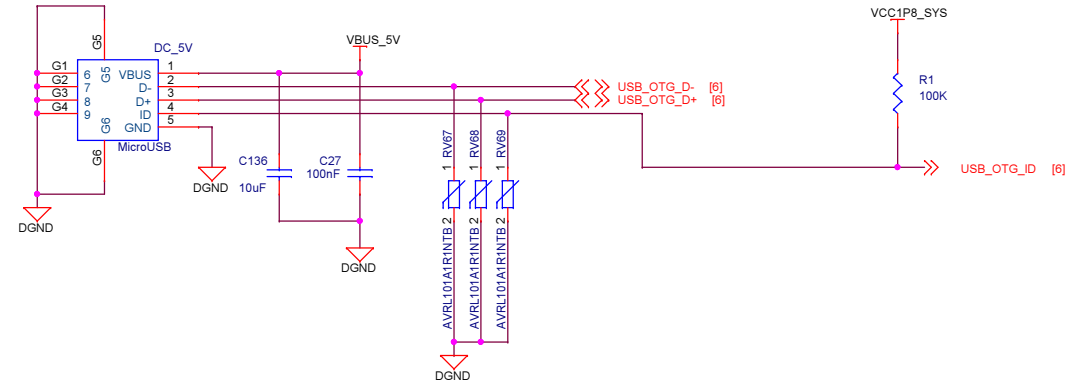


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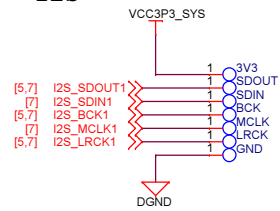
2.54mm Header



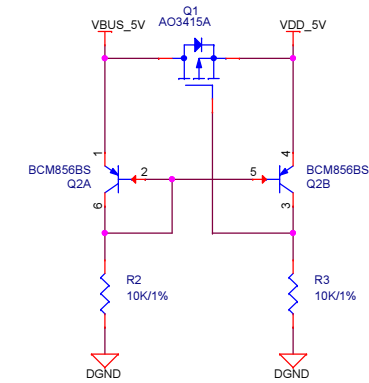
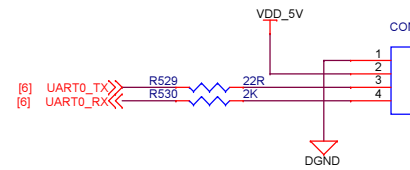
MicroUSB



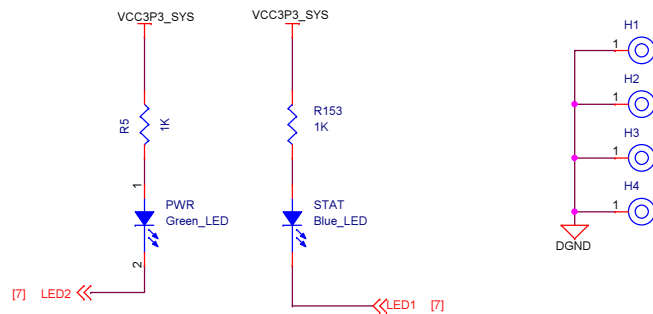
I2S



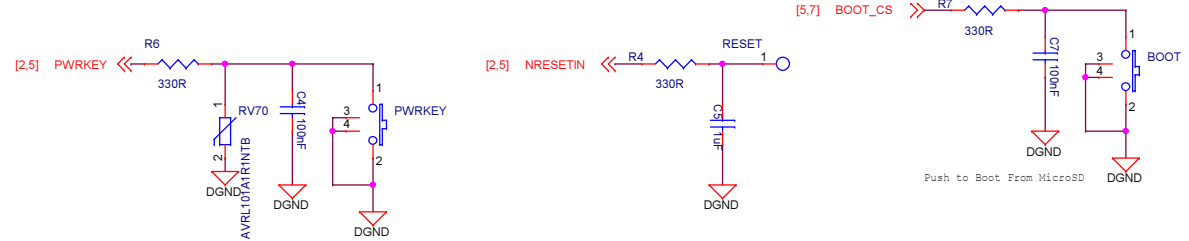
Debug UART



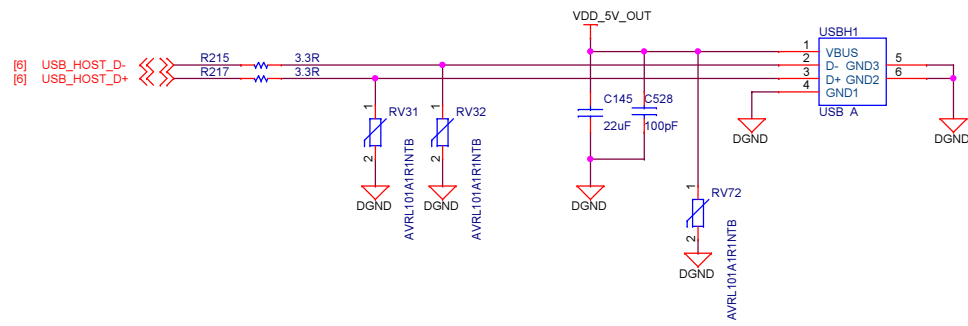
LEDs



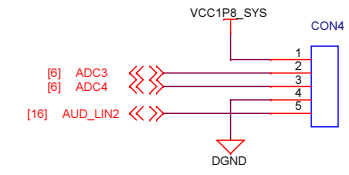
Buttons



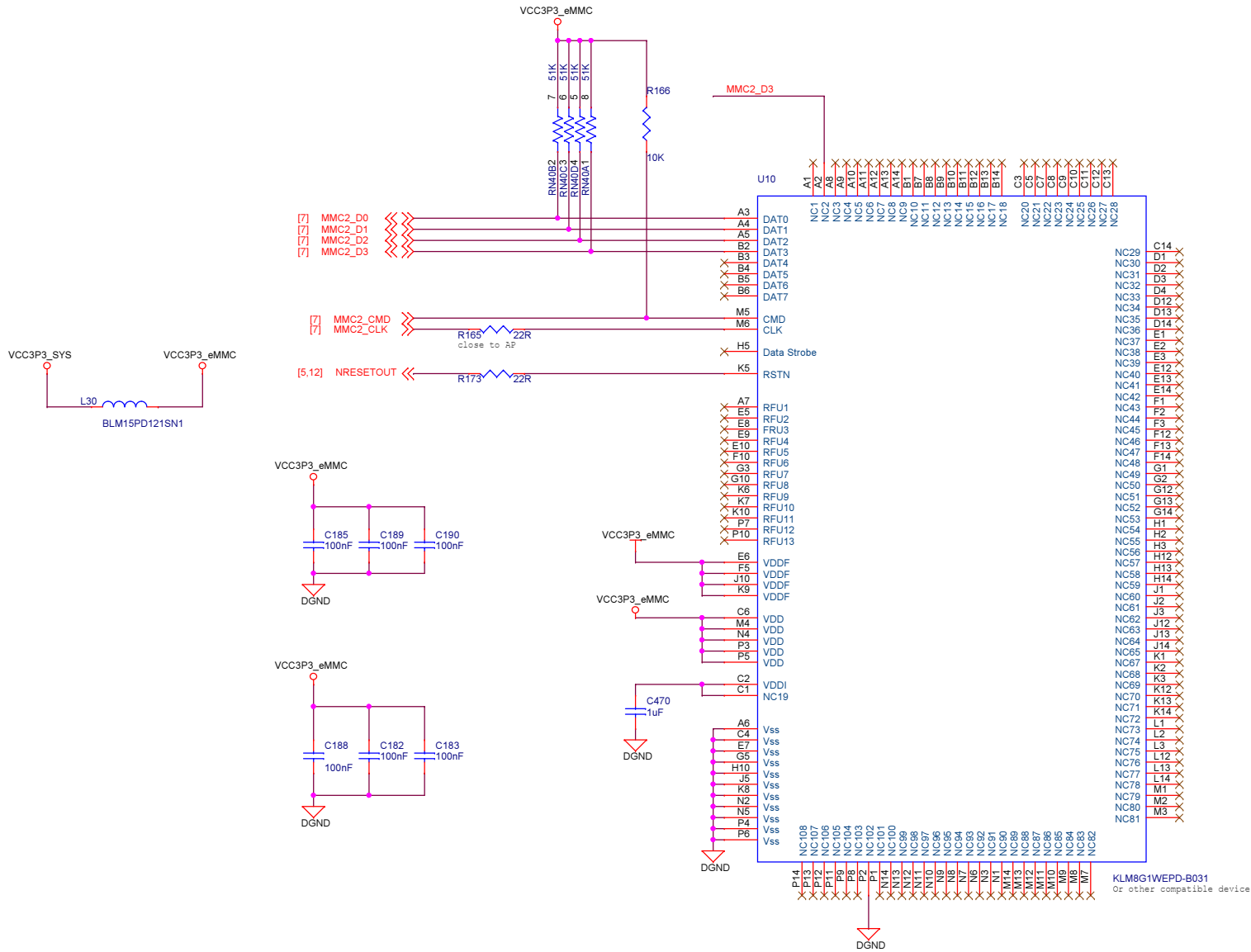
USB 2.0 Host



ADC, Line-IN



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Audio

